

1998P04230 - Application No. 09/868,773
Response to Office action 3/29/2007
Response submitted July 18, 2007

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Amendments to the Claims

Listing of Claims:

Claim 1 – 2 (canceled).

Claim 3 (currently amended). The method ~~as claimed in claim 1~~, further according to claim 6, further comprising: filtering the synchronized digital output signals with an anti-aliasing filter directly after the interpolation.

Claim 4 (canceled).

Claim 5 (currently amended). The method ~~as claimed in claim 3~~ according to claim 6, further comprising: forming the digital auxiliary signals directly from the input signals and using an integrator for the interpolation when the digital input signals are formed from secondary variables of Rogovsky measuring transducers.

Claim 6 (new). A method for a synchronizing a plurality of digital input signals formed by sampling analog secondary input variables of measuring transducers,

wherein the measuring transducers are disposed at various points in an electric power supply system; and

wherein the analog secondary input variables are sampled with a dedicated operating clock;

the method comprising:

encoding each digital input signal using a signal encoder formed by a differentiator forming encoded digital input signals;

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transmitting each encoded digital input signal from a transmission device via a transmission channel to a receiving device;

forming digital auxiliary signals using a signal resampling device by respectively sampling the received digital input signals with a common post-processing clock, which is at least twice as fast as a fastest operating clock; and

forming synchronized digital output signals corresponding to the digital input signals by interpolating each digital auxiliary signal using an interpolation filter formed by an integrator.